

Title (en)

CHIP VARISTOR FOR USE IN PRINTED CIRCUITS, AND METHOD OF PRODUCING IT

Publication

**EP 0171642 B1 19880518 (DE)**

Application

**EP 85109001 A 19850718**

Priority

DE 3428242 A 19840731

Abstract (en)

[origin: EP0171642A1] 1. A chip varistor for use in printed circuits (GS) for installation on conductor path contact surfaces (10, 11) of a printed circuit board (12), consisting of a parallelepiped-shaped ceramic body (20) of varistor material which is provided on oppositely disposed surfaces with solderable metal coatings (23, 24) as electrodes which extend at least on to that surface of the ceramic body (20) which faces towards the printed circuit board (12), characterized in that, at least on its side facing towards the printed circuit board (12), the ceramic body (20) is provided with a recess (25, 26) which forms two lateral parts (27, 28, 31, 32), that parts (35, 36, 37, 38) of the metal coatings (23, 24) extend to the end faces (29, 30, 33, 34) of the lateral parts (27, 28, 31, 32), and that the width and depth of the recess (25, 26) are such that the current path (41) along the surface parts of the recess (25, 26) between the ends (39, 40) of the parts (35, 36, 37, 38) of the metal coatings on the end faces (29, 30, 33, 34) of the lateral parts (27, 28, 31, 32) is longer than the lateral spacing (42) between the metal coatings (23, 24), which corresponds to the thickness of the ceramic body (20).

IPC 1-7

**H01C 7/10**

IPC 8 full level

**H01C 7/10** (2006.01); **H01C 7/102** (2006.01)

CPC (source: EP)

**H01C 7/102** (2013.01)

Cited by

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Designated contracting state (EPC)

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