Title (en)
DISPLAY SYSTEM FOR COMPUTERS

Publication
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Application
EP 85305996 A 19850822

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- US 64313584 A 19840822

Abstract (en)
[origin: EP0175499A2] A CPU communicates over a bus (203) with a liquid crystal display (LCD) device (209) by way of a control unit (217), a display memory (213) and another control unit (219). The memory (213) includes a screen image RAM (223) which is a pixel memory cyclically accessed by the latter control unit (219) to keep the display refreshed. The former control unit (217) keeps the contents of the screen image RAM (223) updated in accordance with ASCII codes in a RAM (221), each with an associated attribute code. The control unit (217) converts ASCII character codes into corresponding groups of pixel codes written into the screen image RAM (223) by reference to a font pattern RAM (225). ASCII graphic codes are passed to the screen image RAM (223) without conversion but, in either event, the attribute codes selectively modify what goes into the screen image RAM (223), on a character by character basis, to determine whether normal or reverse video is employed, blinking characters are employed, a high speed, imperceptible blinking is employed to provide an intermediate visual tone and to implement a form of colour emulation in which colour codes in the attribute codes select the background tone (and complementary foreground tone) of the display.

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G09G 3/3611 (2013.01); G09G 3/2018 (2013.01)

Citation (search report)
[X] 1983 SID INTERNATIONAL SYMPOSIUM, DIGEST OF TECHNICAL PAPERS, 1st edition, May 1983, pages 32-33, SID, Coral Gables, US; Y. SUZUKI et al.: "A liquid-crystal image display"

Cited by
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