

Title (en)
PROGRAMMABLE UNIVERSAL SYNCHRONIZATION BYTE DETECTOR

Publication
EP 0176975 A3 19890118 (EN)

Application
EP 85112257 A 19850927

Priority
US 65771684 A 19841004

Abstract (en)
[origin: EP0176975A2] A programmable synchronization byte detector in a disk controller includes a shift register for serially receiving and shifting in the register a stream of bits read out from a disk unit, a processor for generating a plurality of select signals, a register for storing and making available over a time period one of the select signals, and a multiplexer receiving in input the parallel bit output of the shift register and transferring in output the select bit signal under control of the select signal received from the register. The signal in output from the multiplexer is used as synchronization signal for placing the disk controller in byte synchronization with the stream of bits received from the disk device.

IPC 1-7
G06F 3/06; **G11B 27/30**

IPC 8 full level
G06F 3/06 (2006.01)

CPC (source: EP KR US)
G06F 3/00 (2013.01 - KR); **G06F 3/0601** (2013.01 - EP US); **G06F 13/00** (2013.01 - KR); **G06F 3/0673** (2013.01 - EP US)

Citation (search report)
• [A] EP 0049627 A2 19820414 - HONEYWELL INF SYSTEMS [US]
• [A] EP 0096465 A1 19831221 - MEDIA SYSTEMS TECH [US]
• [A] WIRELESS WORLD, vol. 88, no. 1562, November 1982, pages 70-73, Sheepen Place, Olchester, GB; J.R. WATKINSON: "Disc-drive controllers"

Designated contracting state (EPC)
BE CH DE FR GB IT LI NL SE

DOCDB simple family (publication)
EP 0176975 A2 19860409; **EP 0176975 A3 19890118**; AU 4814985 A 19860410; AU 578976 B2 19881110; BR 8504891 A 19860722; CN 1003327 B 19890215; CN 85108638 A 19861008; DK 451185 A 19860405; DK 451185 D0 19851003; ES 547551 A0 19870501; ES 8705673 A1 19870501; FI 853831 A0 19851003; FI 853831 L 19860405; KR 860003555 A 19860526; KR 920001955 B1 19920307; NO 853920 L 19860407; US 4663733 A 19870505; YU 158785 A 19871231

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