

Title (en)

Disk controller with shared address register.

Title (de)

Plattenspeichersteuergerät mit geteiltem Adressenregister.

Title (fr)

Appareil de commande de disque à registre d'adresse partagé.

Publication

EP 0176976 B1 19931020 (EN)

Application

EP 85112258 A 19850927

Priority

US 65771584 A 19841004

Abstract (en)

[origin: EP0176976A2] Disk controller with shared address register wherein common address register is used to address locations having a common address portion in both a main memory and a disk controller data random access memory (RAM) during a block transfer of data in a disk subsystem. Data bits are received by the disk controller and stored in the RAM as data bytes. The common address register stores the address which represents both the RAM location from which the data bytes are transferred and the main memory location into which the data bytes are stored.

IPC 1-7

G06F 3/06; G06F 5/06; G06F 12/04

IPC 8 full level

G06F 3/06 (2006.01)

CPC (source: EP KR)

G06F 3/06 (2013.01 - KR); **G06F 3/0601** (2013.01 - EP); **G06F 3/0673** (2013.01 - EP)

Cited by

EP0374114A3; EP0684544A1; US5535419A

Designated contracting state (EPC)

DE FR GB IT NL SE

DOCDB simple family (publication)

EP 0176976 A2 19860409; **EP 0176976 A3 19890111**; **EP 0176976 B1 19931020**; AU 4815385 A 19860410; AU 585262 B2 19890615; CA 1252577 A 19890411; CN 1004946 B 19890802; CN 85108598 A 19861015; DE 3587635 D1 19931125; DE 3587635 T2 19940421; DK 167784 B1 19931213; DK 451085 A 19860405; DK 451085 D0 19851003; ES 547550 A0 19870501; ES 8705672 A1 19870501; FI 853830 A0 19851003; FI 853830 A 19860405; FI 87282 B 19920831; FI 87282 C 19921210; KR 860003554 A 19860526; KR 930008268 B1 19930827; NO 171434 B 19921130; NO 171434 C 19930310; NO 853919 L 19860407; YU 157385 A 19871031

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