

Title (en)
ANNUNCIATOR IDENTIFICATION ARRANGEMENT IN AN ALARM SYSTEM

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Application
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Abstract (en)
[origin: EP0178451A1] 1. Annunciator identification arrangement in an alarm system, in particular fire alarm system, having a plurality of closed-circuit protected, two-wire signal lines (ML) connected to a central unit (Z) with an evaluation device (AWE), to which signal lines in each case a plurality of annunciators (M1, M2, ...) are connected, an alarm-triggering annunciator causing a voltage dip in the corresponding line voltage (UL) as a result of a central limitation of the line current (IL) to a first current limit value, and an alarm signal of the corresponding signal line (ML) being derived therefrom in the central unit (Z), and the central unit (Z) outputting current pulses (IPZ) with a second, increased current limit value to the corresponding signal line (ML) characterized in that there is connected upstream of each annunciator (M1, M2, ...) an annunciator attachment circuit arrangement (MZS1, MZS2, ...) which has a bistable multivibrator (FF) having a setting input S which can be acted upon by a first differentiating element (R1, C1) connected to the signal line (ML) and having a reset input (R) which can be acted upon by a second differentiating element (R2, C2) connected to the signal line (ML) and having arranged in a wire (4, 5) of the signal line (ML) a switching element (TR) which can be controlled via a voltage limiter circuit (ZD) by the output (Q) of the bistable multivibrator (FF), the annunciator (M1, M2, ...) being connected to the signal line (ML) via light-emitting diode (LED), in that in the central unit (Z) there is assigned to the evaluation device (AWE) an annunciator identification circuit arrangement (MIS) which has a current measuring device (SME) for monitoring the line current (IL), an analog-digital converter (AD), the first input (E) of which is acted upon by the measured line current, and a microprocessor (MR) with a display device (ANZ) for the annunciator address connected downstream of the analog-digital converter (AD), the output (QW) of the analog-digital converter (AD) supplying the digitized current values to the microprocessor (MR), which controls a further input (F) of the analog-digital converter (AD) and which is acted upon by the central current pulses (IPZ) of the increased line current (flashing pulse), and in that, with each central current pulse (IPZ), in chronological order a limited (ZD) voltage is switched to the annunciator (M1, M2, ...) connected downstream of the respective annunciator attachment circuit arrangement (MZS1, MZS2, ...), the alarm-triggering annunciator causing an increased current flow, on the basis of which the annunciator identification circuit arrangement (MIS) determines and displays (ANZ) the address of the annunciator triggering the alarm.

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