Title (en)

METHOD AND ARRANGEMENT FOR THE DETECTOR IDENTIFICATION OF A HAZARD DETECTION SYSTEM

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Application

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Priority

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Abstract (en)

[origin: EP0178474A2] Several two-wire signalling lines (ML) with in each case several detectors (M1, M2, ...) are connected to a central station (Z). An alarm-triggering detector causes a voltage dip (ULA ML) in the relevant line voltage (UL) and thus an alarm signal (AL ML). Using an addressing chip (AB) which is in each case allocated to one detector (M1, M2, ...), the voltage dip (ULA ML) is first generated time- limited (TV) on the signalling line (ML) and then the line voltage (UL) is disconnected for a short time (RSIP) from the signalling line (ML) by the central station (Z) and then interrogation pulses (AFIP) are output to the signalling line (ML). From the pulse sequence of the interrogation pulses (AFIP), the addressing chip (AB) determines the address of its associated detector, in which process the alarm-triggering detectors (Mi) successively cause a detector alarm (AL Mi) in the form of an abrupt rise in the line current (ILA Mi) when the detector address set is coincident with the determined address. From this, the central station (Z) determines the respective alarm-signalling detectors (Mi) with the corresponding detector address and indicates this. The associated display (MA) at the detector is activated by means of the detector alarm (AL Mi) of the relevant detector (Mi) (Figure 1). <IMAGE>

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