

Title (en)  
CATHODE RAY TUBE DISPLAY SYSTEM

Publication  
**EP 0180593 B1 19890913 (EN)**

Application  
**EP 85901828 A 19850327**

Priority  
• JP 7764384 A 19840419  
• US 67711584 A 19841130

Abstract (en)  
[origin: WO8504976A1] A cathode ray tube display system includes a refresh memory (14) storing data to be displayed and normally accessed by CRT controller (12), but in response to a request signal, accessible by a microprocessor (10). In response to such a request signal, a control unit (32) switches a multiplexer (16) to pass address signals from the microprocessor (10) to the refresh memory (14) and further provides an inhibit signal to a parallel-to-serial convertor (26) in the CRT video drive to prevent any data read out from the memory (14) from being displayed. The CRT has a long persistence time such that flashing or flickering of the display is avoided.

IPC 1-7  
**G09G 1/16**

IPC 8 full level  
**G09G 1/16** (2006.01); **G09G 5/00** (2006.01)

CPC (source: EP)  
**G09G 5/001** (2013.01)

Designated contracting state (EPC)  
DE FR GB

DOCDB simple family (publication)  
**WO 8504976 A1 19851107**; DE 3573037 D1 19891019; EP 0180593 A1 19860514; EP 0180593 B1 19890913

DOCDB simple family (application)  
**US 8500505 W 19850327**; DE 3573037 T 19850327; EP 85901828 A 19850327