

Title (en)

EXTERNAL INTERFACE CONTROL CIRCUITRY FOR MICROCOMPUTER SYSTEMS.

Title (de)

EXTERNE SCHNITTSTELLENSTEUERANORDNUNG FÜR MIKRORECHNERSYSTEME.

Title (fr)

ENSEMBLE DE CIRCUITS DE COMMANDE D'INTERFACE EXTERNE POUR DES SYSTEMES DE MICRO-ORDINATEURS.

Publication

EP 0187159 A4 19861107 (EN)

Application

EP 85903128 A 19850605

Priority

US 61811584 A 19840607

Abstract (en)

[origin: WO8600154A1] External interface control circuitry that couples a microcomputer system (100) to an external device (800). The control circuitry includes a microcomputer (102), a power switch (108) for applying power to the external device (800) in response to a PSC* signal, bus control (104) for gating interface signals from the microcomputer (102) with a PD* signal for application to the external device (800) and a peripheral interface adapter (106) for producing the PC and PD* signals and coupling the PI* and READY*/IRQ* signals to the microcomputer (102). A power-up sequence is executed by the microcomputer (102) in response to grounding the PI* signal when the external device (800) is plugged into the microcomputer system (100). Next, the external device (800) produces a binary zero state of the READY*/IRQ* signal, and in response microcomputer (102) produces a binary one state of the PD* signal and thereafter the PC signal. The sequence is then completed when the external device (800) returns the READY*/IRQ* signal to the binary one state. The unique power-up process insures that the external device (800) is only powered when present and protects CMOS devices in the interface control circuitry from being inadvertently destroyed.

IPC 1-7

G06F 9/00; **G06F 7/00**; **G06F 13/00**

IPC 8 full level

G06F 1/00 (2006.01); **G06F 3/00** (2006.01); **G06F 12/06** (2006.01); **G06F 13/40** (2006.01); **H04L 29/00** (2006.01)

CPC (source: EP KR)

G06F 9/00 (2013.01 - KR); **G06F 12/0684** (2013.01 - EP); **G06F 13/4068** (2013.01 - EP)

Citation (search report)

- [A] GB 2130405 A 19840531 - BRITISH AEROSPACE
- [A] PATENTS ABSTRACTS OF JAPAN, vol. 6, no. 198 (P-147)[1076], 7th October 1982; & JP - A - 57 106 924 (FUJITSU K.K.) 03-07-1982
- [A] PATENTS ABSTRACTS OF JAPAN, vol. 7, no. 274 (P-241)[1419], 7th December 1983; & JP - A - 58 154 026 (HITACHI SEISAKUSHO K.K.) 13-09-1983
- [A] PATENTS ABSTRACTS OF JAPAN, vol. 6, no. 100 (P-121)[978], 9th June 1982; & JP - A - 57 31 018 (FUJITSU K.K.) 19-02-1982
- See references of WO 8600154A1

Designated contracting state (EPC)

AT BE CH DE FR GB LI NL SE

DOCDB simple family (publication)

WO 8600154 A1 19860103; CA 1233909 A 19880308; EP 0187159 A1 19860716; EP 0187159 A4 19861107; IL 75374 A0 19850929; IL 75374 A 19890228; JP S61502429 A 19861023; KR 860700166 A 19860331

DOCDB simple family (application)

US 8501061 W 19850605; CA 483276 A 19850606; EP 85903128 A 19850605; IL 7537485 A 19850531; JP 50271085 A 19850605; KR 860700075 A 19860207