

Title (en)

DATA PROCESSOR HAVING MODULE ACCESS CONTROL.

Title (de)

DATENPROZESSOR MIT MODULZUGRIFFSSTEUERUNG.

Title (fr)

PROCESSEUR DE DONNEES AVEC CONTROLE D'ACCES A DES MODULES.

Publication

EP 0187763 A4 19890530 (EN)

Application

EP 85902340 A 19850424

Priority

US 62636384 A 19840628

Abstract (en)

[origin: WO8600437A1] A data processor (12) cooperates with an access controller (14) to control access to a module stored in a storage device (20). In response to receiving an instruction which requests access to the module and specifies an address within the storage device (20) containing an access request, the data processor (12) retrieves the access request and provides the access request to the access controller (14). The data processor (12) will then initiate the requested access. However, the access will be faulted if the access controller (14) decides to deny the access request.

IPC 1-7

G06F 9/00; **G06F 9/46**

IPC 8 full level

G06F 12/14 (2006.01); **G06F 13/16** (2006.01); **G06F 21/24** (2006.01); **G06F 21/60** (2013.01); **G06F 21/62** (2013.01); **G06F 21/79** (2013.01)

CPC (source: EP KR)

G06F 9/00 (2013.01 - KR); **G06F 13/1605** (2013.01 - EP)

Citation (search report)

- [Y] WO 8402208 A1 19840607 - MOTOROLA INC [US]
- [A] US 4442484 A 19840410 - CHILDS JR ROBERT H E [US], et al
- [X] ELECTRO/81 CONFERENCE RECORD, New York, 7th-9th April 1981, pages 6a/2;1-5, Electronic Conventions, Inc.; R. CHILDS: "The iAPX 286 architecture: memory management and protection model"
- [A] COMMUNICATIONS OF THE ACM, vol. 15, no. 5, May 1972, pages 308-318; A. BENSOUSSAN et al.: "The multics virtual memory: concepts and design"
- See references of WO 8600437A1

Designated contracting state (EPC)

DE FR GB IT NL

DOCDB simple family (publication)

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DOCDB simple family (application)

US 8500735 W 19850424; CA 479216 A 19850416; EP 85902340 A 19850424; JP 50213785 A 19850424; KR 860700123 A 19860227