

Title (en)
DISPLAY CONTROLLER

Publication
EP 0191382 A3 19891129 (EN)

Application
EP 86101276 A 19860131

Priority
JP 2666485 A 19850215

Abstract (en)
[origin: EP0191382A2] The display controller incorporates means (microprocessor 22) to position the cursor on a screen (28) rapidly from its present location to operator selectable fields, whose positions are indicated by special codes in the refresh buffer (24). A special selectable field tab key (33) is provided in the keyboard to activate this function.

IPC 1-7
G09G 1/00

IPC 8 full level
G06F 3/0481 (2013.01); **G06F 3/14** (2006.01); **G09G 5/08** (2006.01)

CPC (source: EP)
G09G 5/08 (2013.01)

Citation (search report)

- [X] US 3524182 A 19700811 - CRISCIMAGNA TONY N
- [A] US 4101879 A 19780718 - KAWAJI MITSUO, et al
- [Y] IBM TECHNICAL DISCLOSURE BULLETIN, vol. 25, no. 1, June 1982, pages 221,222, New York, US; T.L. ADAM et al.: "Cursor left/right/up/down"
- [A] IBM TECHNICAL DISCLOSURE BULLETIN, vol. 22, no. 5, October 1979, pages 2103-2105, New York, US; I.D. JUDD et al.: "Microprocessor-controlled cursors"

Cited by
EP0383938A4; EP0406430A4

Designated contracting state (EPC)
DE FR GB

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EP 0191382 A2 19860820; EP 0191382 A3 19891129; JP H0327916 B2 19910417; JP S61188586 A 19860822

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