

Title (en)

MICROCOMPUTER CONTROLLED LOCKING SYSTEM.

Title (de)

HALBLEITERGESTEUERTES VERRIEGELUNGSSYSTEM.

Title (fr)

SYSTEME DE VERROUILLAGE COMMANDE PAR MICRO-ORDINATEUR.

Publication

EP 0193537 A4 19870212 (EN)

Application

EP 85903111 A 19850604

Priority

- US 64179284 A 19840817
- US 74004085 A 19850531

Abstract (en)

[origin: WO8601360A1] A locking system which comprises a microcomputer controlled lock (10) with multiple level keying, i.e. multiple keys (34) having different functions in the locking system. A key reader (38), for example punch card or magnetic type, reads key code into the microcomputer. A read/write memory (217) has a predetermined number of assigned key codes stored therein and a read-only memory (215) has a control program stored therein for control of the microcomputer. An electrically controlled actuator (42) for the locking means is coupled with an unlocking output of the microcomputer. The control program comprises a main program and a plurality of subroutines each stored at a different location in the read-only memory. A function table (219) has a number of pointers each of which points to the memory address of one of the subroutines. The number of pointers in the function table (219) is equal to the number of locations in the key code memory (217). Decoding means operative under program control in conjunction with a control code (102) on the key (34) points to selected locations in the key code memory (217) and the function table (218) corresponding to the control code (102). A microcomputer (112), under control of the main program, compares the primary (104) and secondary key codes (106) from the selected key (34) with the assigned key at the location in the key code memory (214). If there is a match, the microcomputer then operates under the control of the subroutine at the memory address pointed to by the function table (219) to execute the function of the selected key (34).

IPC 1-7

H04Q 1/39; E05B 45/06; E05B 49/00; E05B 49/02

IPC 8 full level

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CPC (source: EP KR)

E05B 49/00 (2013.01 - KR); **G07C 9/00722** (2013.01 - EP); **G07C 9/00904** (2013.01 - EP)

Citation (search report)

- [A] EP 0043270 A1 19820106 - OMRON TATEISI ELECTRONICS CO [JP]
- [A] US 4283710 A 19810811 - GENEST LEONARD J, et al
- [A] FR 2323851 A1 19770408 - MONITRON IND [US]
- See references of WO 8601360A1

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EP3162988A1; SG87756A1; US10403122B2; US10600313B2; US11721198B2

Designated contracting state (EPC)

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DOCDB simple family (publication)

WO 8601360 A1 19860227; AT E75092 T1 19920515; AU 4493485 A 19860307; BR 8506882 A 19861209; CA 1252854 A 19890418; CA 1276039 C 19901106; DE 3585876 D1 19920521; EP 0193537 A1 19860910; EP 0193537 A4 19870212; EP 0193537 B1 19920415; KR 860700327 A 19860801; KR 930005700 B1 19930624; NO 301434 B1 19971027; NO 861387 L 19860611

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