

Title (en)

ELECTRONIC POSTAGE METER HAVING A MEMORY MAP DECODER AND AN ILLEGAL-MEMORY-ACCESS WARNING SIGNAL, RESPECTIVELY

Publication

EP 0194660 A3 19870429 (EN)

Application

EP 86103281 A 19860312

Priority

- US 71079985 A 19850312
- US 71080085 A 19850312

Abstract (en)

[origin: EP0194660A2] An electronic postage meter having an improved memory selection circuit (CPU) is disclosed. A custom memory map decoder circuit which resolution down to a single byte location is used to provide selection enabling signals to insure the selection of an appropriate device only when the addresses appropriate to that device are communicated. A warning signal is provided whenever an attempt is made to access an illegal or unused address.

IPC 1-7

G07B 17/02

IPC 8 full level

G07B 17/00 (2006.01)

CPC (source: EP)

G07B 17/00362 (2013.01); **G07B 2017/00258** (2013.01); **G07B 2017/00403** (2013.01)

Citation (search report)

- US 3978457 A 19760831 - CHECK JR FRANK T, et al
- GB 2101370 A 19830112 - PHILIPS ELECTRONIC ASSOCIATED [GB]
- US 3938095 A 19760210 - CHECK JR FRANK T, et al

Cited by

EP0658847A1; US5805711A; EP0657821A3; US5729716A; EP0658846A1; US5530840A; EP0660269A3; US5671146A; EP0969421A3; EP0969422A3; EP0969423A3

Designated contracting state (EPC)

CH DE FR GB IT LI NL

DOCDB simple family (publication)

EP 0194660 A2 19860917; EP 0194660 A3 19870429; EP 0194660 B1 19920325; DE 3684496 D1 19920430

DOCDB simple family (application)

EP 86103281 A 19860312; DE 3684496 T 19860312