Title (en)

ADDRESSING LIQUID CRYSTAL CELLS

Publication

EP 0196905 A3 19891018 (EN)

Application

EP 86302382 A 19860401

Priority

GB 8508709 A 19850403

Abstract (en)

[origin: EP0196905A2] A method of addressing a matrix addressed ferroelectric liquid crystal cell is described that uses parallel entry of balanced bipolar data pulses on one set of electrodes to co-operate with serial entry of balanced bipolar strobe pulses on the other set of electrodes. The positive- and negative-going excursions of each data pulse are asymmetric, the excursion of one polarity having 'm' times the amplitude of the other and 1/m<th> the duration.

IPC 1-7

G09G 3/36

IPC 8 full level

G02F 1/133 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP)

G09G 3/3629 (2013.01); G09G 2310/06 (2013.01); G09G 2320/0209 (2013.01)

Citation (search report)

- [APD] EP 0137726 A2 19850417 - INT STANDARD ELECTRIC CORP [US]
- [A] EP 0120732 A1 19841003 - COMMISSARIAT ENERGIE ATOMIQUE [FR]
- [X] PATENT ABSTRACTS OF JAPAN, vol. 9, no. 133 (P-362)[1856], 8th June 1985; & JP-A-60 15 624 (HITACHI SEISAKUSHO K.K.) 26-01-1985 & JP-A-45 91 886 (UMEDA et al.)

Designated contracting state (EPC)

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DOCDB simple family (publication)

EP 0196905 A2 19861008; **EP 0196905 A3 19891018**; AU 5537186 A 19861009; AU 580859 B2 19890202; GB 2173335 A 19861008; GB 2173335 B 19880217; GB 8508709 D0 19850509; JP S61286818 A 19861217

DOCDB simple family (application)

EP 86302382 A 19860401; AU 5537186 A 19860327; GB 8508709 A 19850403; JP 7749586 A 19860403