Title (en)
COLOUR VIDEO SIGNALS CONTROL CIRCUIT FOR A HIGH RESOLUTION VISUALIZATION SYSTEM, AND VISUALIZATION SYSTEM COMPRISING SUCH A CIRCUIT

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EP 0197846 B1 19900221 (FR)

Application
EP 86400664 A 19860327

Priority
FR 8504933 A 19850401

Abstract (en)

[origin: EP0197846A1] 1. A colour video signal control circuit for a high resolution display system, controlled by a picture storage (1), in which the aspect of each picture pixel is encoded by means of an aspect word of several bits, comprising : - a transcoding table storage (2) containing codes associated with a scale of colours and addressed on reading by the aspect word associated with each point to be displayed on the colour screen (4) of this circuit, - digital-to-analog converters (31, 32, 33) receiving each a part of the code read out of the transcoding storage at the address defined by the aspect word, and each delivering a primary colour signal as a function of this code to the screen (4), characterized in that the control circuit further comprises : - a set (5) of table buffer storages (51, 52, 53, 54) initially loaded by other codes associated with other scales of colours, and - a control circuit (7) receiving the screen scanning return signal (SYT) as well as blinking signals (HC1 , HC2), the contents of each table buffer storage being susceptible to be transferred into the transcoding table storage (2) during a return period of the picture scanner under control of the control circuit (7).

IPC 1-7
G09G 1/28

IPC 8 full level
G09G 5/06 (2006.01)

CPC (source: EP)
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Cited by
EP0855693A1; EP0266506A3; EP0354480A3; EP0537881A3; GB2195519A; US4823120A; GB2195519B; WO8901218A1; WO0116930A1

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