Title (en)

TESTABLE VIDEO DISPLAY GENERATOR

Publication

EP 0202865 B1 19911106 (EN)

Application

EP 86303691 A 19860515

Priority

US 73524185 A 19850517

Abstract (en)

[origin: EP0202865A2] A display generation system comprises a memory 22 with a display portion storing display information, and an inactive portion storing test data. Scan logic 20 controls a monitor and accesses the memory at locations corresponding to the position of a scan beam of the monitor. A generator 16, 26, 28, 30, 32 generates display control information to provide information control signals to the scan beam, so displaying the information stored in the display portion of the memory. A register 34, 36 stores display control information generated from the test data stored in the inactive portion of the memory, the register being enabled by a control signal generated from the scan logic indicating the end of a display frame. The scan logic is operative during a period of time that the monitor is blanked to allow the scan beam of the monitor to be positioned to a beginning point of the display. An element 10 compares the display control information stored in the register corresponding to the test data with an expected result, the comparison being performed during the period of time the monitor is blanked, thereby verifying on-line that the display generation system is functioning correctly.

IPC 1-7

G06F 11/26

IPC 8 full level

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CPC (source: EP)

G09G 3/006 (2013.01)

Citation (examination)

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DOCDB simple family (publication)

EP 0202865 A2 19861126; **EP 0202865 A3 19880914**; **EP 0202865 B1 19911106**; AU 5712186 A 19861120; AU 579928 B2 19881215; CA 1254683 A 19890523; DE 3682322 D1 19911212; JP H0642132 B2 19940601; JP S61267087 A 19861126; NO 169926 B 19920511; NO 169926 C 19920819; NO 861057 L 19861118; SG 2392 G 19920320; ZA 862964 B 19861230

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