

Title (en)
RELP VOCODER IMPLEMENTED IN DIGITAL SIGNAL PROCESSORS.

Title (de)
RELP-VOKODER ZUR VERWENDUNG IN DIGITALSIGNALPROZESSOREN.

Title (fr)
VOCODER RELP POUR PROCESSEURS DE SIGNAUX NUMERIQUES.

Publication
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Application
EP 85905709 A 19851101

Priority
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Abstract (en)
[origin: WO8602726A1] A residual-excited linear prediction (RELP) vocoder implemented in two digital signal processors, one for a transmitter system (Fig. 1) and the other for a remotely located receiver system (Fig. 2). The transmitter processes digital speech data signal samples to provide a formatted transmission signal including (a) a quantized residual signal generated by inverse filtering of the samples in accordance with linear predictive coding (LPC) coefficients generated from the samples, (b) quantized LPC coefficients, and (c) pitch and gain parameters generated during quantization of the residual signal from the inverse filtered samples, all of which are generated by the processor from the digital speech data signal samples. The receiver digital signal processor is adapted for processing the formatted transmission signal to synthesize reconstructed digital speech data signals. Transmitter and receiver systems that are commonly located may be included in a single digital signal processor.

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G01L 5/00

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

- [X] ICASSP 84 - IEEE INTERNATIONAL CONFERENCE ON ACOUSTICS, SPEECH, AND SIGNAL PROCESSING, 19th-21st March 1984, San Diego, US, vol. 2, pages 27.8.1-27.8.4, IEEE, New York, US; M. DANKBERG et al.: "Implementation of the RELP Vocoder using the TMS320"
- [XP] ICASSP 85 - IEEE INTERNATIONAL CONFERENCE ON ACOUSTICS, SPEECH, AND SIGNAL PROCESSING, 26th-29th March 1985, Tampa, US, vol. 3, pages 969-972, IEEE, New York, US; R.L. ZINSER: "An efficient pitch-aligned high-frequency regeneration technique for RELP Vcoders"
- See references of WO 8602726A1

Designated contracting state (EPC)

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DOCDB simple family (publication)

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