

Title (en)

Control circuit of raster scanning display unit.

Title (de)

Steuergerät für eine nach dem Rasterverfahren arbeitende Anzeigeeinheit.

Title (fr)

Dispositif de commande d'un appareil d'affichage à balayage à trame.

Publication

EP 0206328 A2 19861230 (EN)

Application

EP 86108650 A 19860625

Priority

JP 13683885 A 19850625

Abstract (en)

A control circuit of a raster scanning display unit has an address converting part disposed between a video memory (2) and a read address generating part, said address converting part (5) judging whether or not the area in concern is an area where superposed display is effected. Based on a result of the judgement, the control circuit transfers an address from the read address generating part as it is when the area in concern is outside said area, while it adds, when within said area, a difference between a head storage address of superposing display data and that of superposed display data to the address from said read address generating part to transfer into the video memory (2).

IPC 1-7

G09G 1/00; **G09G 1/16**

IPC 8 full level

G09G 1/00 (2006.01); **G09G 1/16** (2006.01); **G09G 5/00** (2006.01); **G09G 5/14** (2006.01); **G09G 5/395** (2006.01)

CPC (source: EP)

G09G 5/395 (2013.01)

Cited by

EP0590807A3; EP0274439A3; US4903013A; US5623315A; US5692099A; US5694518A; US5831681A; US5845242A; EP2096836A1; US6453286B1

Designated contracting state (EPC)

AT DE GB SE

DOCDB simple family (publication)

EP 0206328 A2 19861230; **EP 0206328 A3 19900502**; **EP 0206328 B1 19930609**; AT E90469 T1 19930615; DE 3688540 D1 19930715; DE 3688540 T2 19931007; JP S61295594 A 19861226

DOCDB simple family (application)

EP 86108650 A 19860625; AT 86108650 T 19860625; DE 3688540 T 19860625; JP 13683885 A 19850625