

Title (en)
DEFECTIVE ELEMENT DISABLING CIRCUIT HAVING A LASER-BLOWN FUSE

Publication
EP 0213044 A3 19890322 (EN)

Application
EP 86401808 A 19860813

Priority
US 76740485 A 19850820

Abstract (en)
[origin: EP0213044A2] A decoder circuit for disabling a defective element in a circuit having redundant replacement elements employs a fuse on a signal path through the decoder circuit from a selection device to a low-impedance output device and a weak default circuit, such as a pull-up transistor, for forcing the state of the output circuit to a low-impedance default state when the fuse is blown.
[origin: EP0213044A2] The RAM includes a memory matrix having rows each with an address within a range and an activator for applying a control signal to the row in response to a given address within the range. The RAM also includes two redundant row lines each having an activator comprising a programmable address recogniser for responding to a programmed defective row address within the range that identifies a defective row in the matrix. Programmable inactivators are connected to the row activators, with each electrically independent of that one of the address recognisers for responding to a programmed defective row address. Each inactivator comprises a fuse (122) for blocking a signal from a decoder unit (120), responsive to an input row address to a control line activating unit (126), and a transistor (125) for forging the latter unit to a default state. The unit includes a low-impedance output directly connected to the control line (127) and is in one of two low-impedance states.

IPC 1-7
G06F 11/20

IPC 8 full level
G11C 29/00 (2006.01); **G11C 29/04** (2006.01)

CPC (source: EP)
G11C 29/785 (2013.01); **G11C 29/83** (2013.01); **G11C 29/832** (2013.01); **G11C 29/84** (2013.01)

Citation (search report)
• [X] EP 0131930 A2 19850123 - TOSHIBA KK [JP], et al
• [XP] GB 2156553 A 19851009 - SHARP KK
• [A] US 4651030 A 19870317 - MIMOTO TOSHIO [JP]

Cited by
EP0974905A3; EP0469252A1; US6215715B1

Designated contracting state (EPC)
AT DE FR GB IT NL

DOCDB simple family (publication)
EP 0213044 A2 19870304; **EP 0213044 A3 19890322**; DE 3650442 D1 19960104; DE 3650442 T2 19960425; EP 0480915 A1 19920415; EP 0480915 B1 19951122; JP 3073747 B2 20000807; JP S6292200 A 19870427

DOCDB simple family (application)
EP 86401808 A 19860813; DE 3650442 T 19860813; EP 92200063 A 19860813; JP 19511986 A 19860820