

Title (en)
CONNECTOR FOR PRINTED CIRCUIT BOARDS

Publication
EP 0213446 A3 19890222 (DE)

Application
EP 86110867 A 19860806

Priority
DE 3528912 A 19850812

Abstract (en)
[origin: US4717344A] A daughterboard connector (6) has a polarity key formed by two walls (14, 15) on either side of a recess (12). A lateral contact (8) mounted in the recess engages a bus bar (3, 4) which is mounted in a pair of slots (20) in the end of a motherboard connector (11). A flange (7) adjacent one of the slots forms a polarity key for the motherboard connector. The height and positioning of the lateral contact and the bus bar are chosen to provide a first-make-last-break function for these elements relative to the signal contacts 19 and 16 of the daughterboard and motherboard connectors, respectively. The bus bar (3, 4) may be common to a number of motherboard connectors which are mounted on a common printed circuit board.

IPC 1-7
H01R 23/68

IPC 8 full level
H01R 12/16 (2006.01); **H01R 12/18** (2006.01); **H01R 12/70** (2011.01)

CPC (source: EP US)
H01R 12/7088 (2013.01 - EP US)

Citation (search report)
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Designated contracting state (EPC)
BE DE FR GB IT NL SE

DOCDB simple family (publication)
EP 0213446 A2 19870311; EP 0213446 A3 19890222; EP 0213446 B1 19920129; DE 3528912 A1 19870219; DE 3528912 C2 19880407; DE 3683706 D1 19920312; SG 37294 G 19941028; US 4717344 A 19880105

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