

Title (en)
A SYNCHRONIZING SYSTEM

Publication
EP 0217486 B1 19901003 (EN)

Application
EP 86304507 A 19860612

Priority
US 78186885 A 19850927

Abstract (en)
[origin: EP0217486A2] A synchronizing system is provided for reliably passing data across a boundary between two independent, non-correlated clocks, referred to as the receiving and transmitting clocks. The system reduces occurrence of errors due to asynchronous samplings to an arbitrarily low level based on metastable operation. The system is organized as a two port memory with unit distance code addressing the memory cells. It performs a handshake between the two non-correlated clock systems to allow for any ratio between the two clocks.

IPC 1-7
G06F 5/06; **G06F 13/42**; **H04L 7/00**

IPC 8 full level
G06F 5/10 (2006.01); **G06F 5/12** (2006.01); **G06F 13/38** (2006.01); **G06F 13/42** (2006.01); **H04L 7/00** (2006.01)

CPC (source: EP)
G06F 5/10 (2013.01); **G06F 13/4243** (2013.01); **G06F 2205/102** (2013.01)

Cited by
EP0411759A3; DE4417286A1; US5867695A; US7428651B2; EP1396786A1; EP0383260A3; EP0380845A3; US5146564A; DE3941880A1; US5274647A; EP0391584A3; EP0540665A4; US5640366A; EP0707260A1; FR2725825A1; GB2289146A; GB2289146B; EP0677974A3

Designated contracting state (EPC)
DE FR GB IT

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EP 86304507 A 19860612; CA 509028 A 19860513; DE 3674700 T 19860612; JP 22949086 A 19860926