

Title (en)
LED MATRIX DISPLAY PANEL

Publication
EP 0221786 B1 19920325 (FR)

Application
EP 86401754 A 19860805

Priority
FR 8514475 A 19850930

Abstract (en)
[origin: EP0221786A2] LED display panel for forming a luminous journal. <??>The LEDs (11) which are arranged in a matrix are controlled via flip-flops forming a high-speed CMOS technology memory (12) able to deliver to them a current of sufficient intensity. The diodes are connected directly to the outputs of these memories and the current crossing them is determined by the supply voltage for the memories (12). <??>The memories are loaded by a shift register (13) of serial type. <IMAGE>

IPC 1-7
G09G 3/14; G09G 3/32

IPC 8 full level
G09G 3/14 (2006.01); **G09G 3/32** (2006.01)

CPC (source: EP US)
G09G 3/14 (2013.01 - EP US); **G09G 3/2085** (2013.01 - EP US); **G09G 3/32** (2013.01 - EP US)

Cited by
EP0755041A1; DE4117889A1; DE4117889C5

Designated contracting state (EPC)
AT BE CH DE GB IT LI LU NL SE

DOCDB simple family (publication)
EP 0221786 A2 19870513; EP 0221786 A3 19890322; EP 0221786 B1 19920325; AT E74225 T1 19920415; DE 3684556 D1 19920430; FR 2588112 A1 19870403; FR 2588112 B1 19891229; NO 863528 D0 19860903; NO 863528 L 19870331; US 4982182 A 19910101

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