

Title (en)  
HIGH SPEED DIGITAL FREQUENCY COUNTER

Publication  
**EP 0224563 A4 19901128 (EN)**

Application  
**EP 86903860 A 19860523**

Priority  
US 73868685 A 19850528

Abstract (en)  
[origin: WO8607156A1] A method and apparatus for computing frequency of an unknown signal using the number of counts of a high and low frequency reference signal which occur between the leading and trailing edges of the pulse of unknown frequency. Method and apparatus is also described utilizing this technique for application in control processes where it is desired to optimize the process by allowing it run at a faster rate and slowing the process as it approaches the desired steady state or other control point in the process.

IPC 1-7  
**G01R 23/02**; **G01R 23/10**; **C23C 14/00**; **C23C 16/00**; **C23C 18/00**; **B05D 3/04**; **B05D 3/10**

IPC 8 full level  
**G01R 23/10** (2006.01); **C23C 14/54** (2006.01); **C23C 16/52** (2006.01); **G01R 23/02** (2006.01)

CPC (source: EP KR)  
**C23C 14/542** (2013.01 - EP); **C23C 16/52** (2013.01 - EP); **G01R 23/02** (2013.01 - EP KR)

Citation (search report)

- DE 3336359 A1 19850418 - ELSTER AG [DE]
- Wireless World, Vol. 79, No. 1449, March 1973, pages 108-111; D.E. O'N WADDINGTON: "Digital multimeter", Figure 2.
- See references of WO 8607156A1

Designated contracting state (EPC)  
AT BE CH DE FR GB IT LI LU NL SE

DOCDB simple family (publication)  
**WO 8607156 A1 19861204**; AU 5952186 A 19861224; EP 0224563 A1 19870610; EP 0224563 A4 19901128; ES 555416 A0 19880216; ES 8801866 A1 19880216; JP S62503056 A 19871203; KR 880700272 A 19880222

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**US 8601098 W 19860523**; AU 5952186 A 19860523; EP 86903860 A 19860523; ES 555416 A 19860528; JP 50305686 A 19860523; KR 870700066 A 19870127