

Title (en)
METHOD TO CONTROL THE MEMORY ACCESS OF AN IC CARD AND IMPLEMENTING DEVICE

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EP 0224639 B1 19910724 (DE)

Application
EP 86108602 A 19860624

Priority
DE 3524371 A 19850708

Abstract (en)
[origin: US4819204A] A method for controlling memory access to a user area and an initial code area of a main memory of a chip card includes carrying out an internal release procedure with a data comparison of an initial code from the initial code area and a data word from a terminal; firmly coupling addresses of the main memory and of a control memory to each other; marking several storage locations of the main memory as the initial code area with one control bit at a time in the control memory; marking a first code deposited in the associated storage location of the first code area as activated or deactivated with one further control bit at a time in the control memory; generating an initial release signal in a release procedure only if a storage location is addressed by an activated initial code and if agreement with the data word entered by the terminal prevails; and preventing generation of the initial release signal if a deactivated code word is addressed and/or if the respective first code does not agree with the data word, and an apparatus for carrying out the method.

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G06F 12/14; **G07F 7/10**

IPC 8 full level
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CPC (source: EP US)
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Cited by
US5497462A; WO9610812A1

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