

Title (en)  
ELECTRONIC POSTAGE METER

Publication  
**EP 0225068 A3 19871014 (EN)**

Application  
**EP 86308638 A 19861105**

Priority  
GB 8529425 A 19851129

Abstract (en)  
[origin: EP0225068A2] A microprocessor in an electronic postage meter is mounted in a hybrid circuit 10 together with various peripheral circuits, so that the complete control circuit of the postage meter is essentially within the one circuit. The hybrid circuit 10 is mounted on a mother board 16 resulting in easy replacement and also leading to a very compact arrangement.

IPC 1-7  
**G07B 17/02**; H01L 27/12

IPC 8 full level  
**G07B 17/00** (2006.01)

CPC (source: EP)  
**G07B 17/00193** (2013.01); **G07B 17/00314** (2013.01); **G07B 2017/00258** (2013.01); **G07B 2017/00322** (2013.01)

Citation (search report)

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**EP 86308638 A 19861105**; GB 8529425 A 19851129; JP 28054686 A 19861125