

Title (en)

ABSTRACT OPERATION-SIGNALLING FROM A RASTER SCAN VIDEO CONTROLLER TO A DISPLAY MEMORY

Publication

EP 0228136 A3 19900321 (EN)

Application

EP 86202317 A 19861218

Priority

US 81536285 A 19851230

Abstract (en)

[origin: EP0228136A2] A generalized operation-signalling logic for a raster scan video controller whereby the video controller, in accessing the memory of a computer system, can indicate what type of operation is to be performed at each memory address to be accessed. The signalling scheme provides a rich set of functions, logic to generate this signalling and an external PLA-type device which interprets this signalling for a wide variety of memory types.

IPC 1-7

G09G 1/16

IPC 8 full level

G06F 3/153 (2006.01); **G09G 5/00** (2006.01); **G09G 5/39** (2006.01); **G09G 5/36** (2006.01)

CPC (source: EP)

G09G 5/39 (2013.01); **G09G 5/363** (2013.01)

Citation (search report)

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- [A] US 4237543 A 19801202 - NISHIO YOJI, et al
- [A] FR 2338531 A1 19770812 - NUGRAPHICS INC [US]
- [A] IBM TECHNICAL DISCLOSURE BULLETIN, vol. 28, no. 5, October 1985, pages 2240-2243, New York, US; "Circuit for updating bit map-memory of a display adapter"

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EP0393722A3

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

EP 0228136 A2 19870708; EP 0228136 A3 19900321; JP S62294284 A 19871221

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