

Title (en)

Cursor circuit for a dual port memory.

Title (de)

Kursorschaltung für einen Speicher mit zwei Eingängen.

Title (fr)

Circuit de curseur pour une mémoire à deux entrées.

Publication

**EP 0229986 A2 19870729 (EN)**

Application

**EP 86117314 A 19861212**

Priority

US 82048786 A 19860117

Abstract (en)

A cursor generation circuit for an image display system that includes a storing circuit 1 for storing image data. The storing circuit includes a first port 8 to provide access to the image data to the display system and a second port 12 to provide access to the image data to a display device 10 for displaying the image data. A combining circuit 22 is further provided to combine the image data with cursor data in the storing circuit when the storing circuit is being accessed by the display device. However, the combining circuit removes the cursor data from the image data when the image data is being accessed by the display system through the first port.

IPC 1-7

**G09G 1/00**

IPC 8 full level

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CPC (source: EP)

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