

Title (en)

CURSOR CIRCUIT FOR A DUAL PORT MEMORY

Publication

EP 0229986 A3 19891227 (EN)

Application

EP 86117314 A 19861212

Priority

US 82048786 A 19860117

Abstract (en)

[origin: EP0229986A2] A cursor generation circuit for an image display system that includes a storing circuit 1 for storing image data. The storing circuit includes a first port 8 to provide access to the image data to the display system and a second port 12 to provide access to the image data to a display device 10 for displaying the image data. A combining circuit 22 is further provided to combine the image data with cursor data in the storing circuit when the storing circuit is being accessed by the display device. However, the combining circuit removes the cursor data from the image data when the image data is being accessed by the display system through the first port.

IPC 1-7

G09G 1/00

IPC 8 full level

G09G 5/08 (2006.01)

CPC (source: EP)

G09G 5/08 (2013.01)

Citation (search report)

- [AP] US 4566000 A 19860121 - GOLDMAN CRAIG E [US], et al
- [A] IBM TECHNICAL DISCLOSURE BULLETIN, vol. 26, no. 10B, March 1984, pages 5622-5623, New York, US; A.C. GAY: "Cursor display on raster graphic devices"
- [A] IEEE COMPCON SPRING '85 DIGEST OF PAPERS, San Francisco, California, 25th-28th February 1985, pages 276-279, IEEE, New York, US; K. GUTTAG et al.: "New silicon to solve fundamental graphics problems"

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GB2252224A; US4987551A; AU611521B2; GB2243521A; GB2243521B; WO8906030A1

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