

Title (en)

Microprocessor systems for electronic postage arrangements.

Title (de)

Mikroprozessorsysteme für elektronische Frankiereinrichtungen.

Title (fr)

Systèmes à microprocesseur pour dispositif d'affranchissement électronique.

Publication

**EP 0231452 A2 19870812 (EN)**

Application

**EP 86116058 A 19830125**

Priority

- EP 83100639 A 19830125
- US 34387782 A 19820129

Abstract (en)

A microprocessor system suitable for an electronic postage arrangement includes a microprocessor (10) and register means (20, 21) for storing accounting data. Sensors (50, 51, 52) are provided for detecting operating conditions in the system and the microprocessor (10) has a software routine stored in permanent memory (11) for periodic checking of the sensors. If the sensor outputs do not correspond with a unique code stored in permanent memory (11), an error condition is indicated.

IPC 1-7

**G07B 17/02**

IPC 8 full level

**G07B 17/00** (2006.01)

CPC (source: EP)

**G07B 17/00362** (2013.01); **G07B 2017/00411** (2013.01)

Citation (applicant)

- US 8941379 A 19791030
- US 4301507 A 19811117 - SODERBERG JOHN H, et al
- US 3978457 A 19760831 - CHECK JR FRANK T, et al

Cited by

US5771348A; DE19534529A1; DE19534529C2; DE19534527A1; DE19534527C2; US5710721A; EP0716398A3; EP1154382A3; EP1154382A2; EP0716398A2

Designated contracting state (EPC)

BE CH DE FR GB LI NL

DOCDB simple family (publication)

**EP 0231452 A2 19870812; EP 0231452 A3 19890215; EP 0231452 B1 19940413; EP 0231452 B2 20020116**

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