Title (en)

TUNER CIRCUIT

Publication

**EP 0234103 A3 19890308 (EN)**

Application

**EP 86309318 A 19861128**

Priority

JP 26601685 A 19851128

Abstract (en)

[origin: EP0234103A2] A tuner circuit for converting the frequency of an input signal with a local oscillator (80) to a predetermined channel frequency. The tuner circuit includes an attenuation circuit (10, 30) responsive to the input signal for controlling the attenuated level of the input signal for reducing cross modulation and intermodulation distortions of the circuit, and for reducing distortion from the interference of the local oscillation, an amplifier circuit (50) coupled to the attenuation circuit (10, 30) for amplifying the attenuated input signal, and a frequency conversion circuit (70) coupled to the amplifier circuit (70) for converting the frequency of the amplified input signal to a corresponding predetermined channel frequency.

IPC 1-7

**H03D 7/00**; H04N 5/44

IPC 8 full level

**H04B 15/06** (2006.01); **H03D 7/00** (2006.01); **H04B 1/18** (2006.01); **H04B 1/26** (2006.01); H03D 7/12 (2006.01)

CPC (source: EP US)

**H03D 7/00** (2013.01 - EP US); **H04N 5/44** (2013.01 - EP US); H03D 7/125 (2013.01 - EP US); H03D 2200/0027 (2013.01 - EP US); H03D 2200/0088 (2013.01 - EP US); H03D 2200/009 (2013.01 - EP US)

Citation (search report)

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- [A] MICROWAVE JOURNAL, vol. 28, no. 10, October 1985, pages 143-146, Dedham, Massachusetts, US; J.S. BHARJ et al.: "17/12 GHz low noise reciever for UNISAT TV DBS satellite"

Designated contracting state (EPC)

DE GB

DOCDB simple family (publication)

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DOCDB simple family (application)

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