

Title (en)  
CIRCUIT ARRANGEMENT FOR REGULATING HIGH VOLTAGES

Publication  
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Application  
**EP 87200077 A 19870119**

Priority  
DE 3601857 A 19860123

Abstract (en)  
[origin: US4757251A] The invention relates to a circuit arrangement for adjusting a high voltage. According to the invention, several semiconductor adjustment members are connected in series, only one of the adjustment members being directly coupled to a control signal source, while the control voltage for the remaining adjustment members is each time derived from the output voltage of the preceding adjustment member.

IPC 1-7  
**G05F 1/595**; **H05G 1/32**

IPC 8 full level  
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CPC (source: EP US)  
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Cited by  
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**DE 3601857 A1 19870730**; DE 3779932 D1 19920730; EP 0234610 A2 19870902; EP 0234610 A3 19891220; EP 0234610 B1 19920624; JP S62219116 A 19870926; US 4757251 A 19880712

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