

Title (en)
Data display.

Title (de)
Datenanzeige.

Title (fr)
Affichage de données.

Publication
EP 0238113 A2 19870923 (EN)

Application
EP 87200221 A 19870212

Priority
GB 8603850 A 19860217

Abstract (en)
A logic processor-controlled data display apparatus having a display memory in which pixel data representing text for display is stored in bit-map form. The pixel data is produced for each display from character data stored in a background memory. When the character data is stored in bit-map form it can be read directly from the background memory bit-by-bit and written into the display memory as the pixel data. However, this process takes a large number of programme steps, so that the transfer of the data is relatively slow. The present invention provides for the storage in the background memory of character data in the form of machine code sub-routines. The sub-routine for a character contains instructions for identifying the shape-defining pixels (dots) of the character relative to a base dot position and the sub-routine is run to write these pixels into the display memory following location of the base dot position in the display memory. Figures 2 and 4 exemplify the invention.

IPC 1-7
G09G 1/16

IPC 8 full level
G06F 3/153 (2006.01); **G09G 1/02** (2006.01); **G09G 1/16** (2006.01); **G09G 5/22** (2006.01); **G09G 5/24** (2006.01); **G09G 5/36** (2006.01); **G09G 5/393** (2006.01)

CPC (source: EP)
G09G 5/24 (2013.01)

Cited by
CN110675800A

Designated contracting state (EPC)
DE FR GB IT SE

DOCDB simple family (publication)
GB 2186766 A 19870819; **GB 8603850 D0 19860326**; DE 3750766 D1 19950105; DE 3750766 T2 19950622; EP 0238113 A2 19870923; EP 0238113 A3 19910717; EP 0238113 B1 19941123; JP H083704 B2 19960117; JP S62249188 A 19871030

DOCDB simple family (application)
GB 8603850 A 19860217; DE 3750766 T 19870212; EP 87200221 A 19870212; JP 3262887 A 19870217