

Title (en)  
DATA DISPLAY

Publication  
**EP 0238113 A3 19910717 (EN)**

Application  
**EP 87200221 A 19870212**

Priority  
GB 8603850 A 19860217

Abstract (en)  
[origin: GB2186766A] A logic processor-controlled data display apparatus has a display memory in which pixel data representing text for display is stored in bit-map form. The pixel data is produced for each display from character data stored in a background memory. The character data is stored in the form of machine code sub-routines. The sub-routine for a character contains instructions for identifying the shape-defining pixels (dots) of the character relative to a base dot position and the sub-routine is run to write these pixels into the display memory following location of the base dot position in the display memory. The invention requires fewer programme steps than when character data is stored in the background memory in bit-map form. <IMAGE>

IPC 1-7  
**G09G 1/16**

IPC 8 full level  
**G06F 3/153** (2006.01); **G09G 1/02** (2006.01); **G09G 1/16** (2006.01); **G09G 5/22** (2006.01); **G09G 5/24** (2006.01); **G09G 5/36** (2006.01); **G09G 5/393** (2006.01)

CPC (source: EP)  
**G09G 5/24** (2013.01)

Citation (search report)  
[A] GB 2007413 A 19790516 - DIAB K M

Cited by  
CN110675800A

Designated contracting state (EPC)  
DE FR GB IT SE

DOCDB simple family (publication)  
**GB 2186766 A 19870819**; **GB 8603850 D0 19860326**; DE 3750766 D1 19950105; DE 3750766 T2 19950622; EP 0238113 A2 19870923; EP 0238113 A3 19910717; EP 0238113 B1 19941123; JP H083704 B2 19960117; JP S62249188 A 19871030

DOCDB simple family (application)  
**GB 8603850 A 19860217**; DE 3750766 T 19870212; EP 87200221 A 19870212; JP 3262887 A 19870217