

Title (en)

CIRCUIT ARRANGEMENT FOR OPERATING LOW-PRESSURE DISCHARGE LAMPS

Publication

**EP 0239793 B1 19910703 (DE)**

Application

**EP 87102736 A 19870226**

Priority

DE 3608615 A 19860314

Abstract (en)

[origin: US4710682A] A circuit for operating low-pressure discharge lamps at elevated frequency comprises a reconnect circuit which disables the disconnect circuit upon exchange of a defective low-pressure discharge lamp (LP1). The disconnect circuit comprises a diode (D10), a resistor (R11) and a thyristor (TH) together with a trigger circuit (5). The reconnect circuit comprises a capacitor (C9) and a resistor (R12). Upon removal of the defective low-pressure discharge lamp (LP1), the capacitor (C9) is charged over the resistor (R12). Upon insertion of a new low-pressure discharge lamp (LP1), the capacitor (C9) is discharged and recharged in opposite direction and the holding current is removed from the thyristor (TH). This causes the thyristor (TH) to block, and enables the push-pull frequency generator (3) to start oscillating again.

IPC 1-7

**H05B 41/29**

IPC 8 full level

**H02M 7/5375** (2006.01); **H05B 41/24** (2006.01); **H05B 41/282** (2006.01); **H05B 41/298** (2006.01)

CPC (source: EP US)

**H05B 41/2985** (2013.01 - EP US); **Y10S 315/07** (2013.01 - EP US)

Cited by

EP0330118A3; EP0372661A1; DE19715341C1; US6043612A; EP0466245A3; EP0395159A1; GB2211038A; DE19715342C1; US6011358A; EP0659037A3; WO9009087A1

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**US 4710682 A 19871201**; AT E65009 T1 19910715; DE 3608615 A1 19870917; DE 3771099 D1 19910808; EP 0239793 A1 19871007; EP 0239793 B1 19910703; FI 870901 A0 19870302; FI 870901 A 19870915; SU 1574187 A3 19900623; ZA 871394 B 19870930

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**US 2345687 A 19870309**; AT 87102736 T 19870226; DE 3608615 A 19860314; DE 3771099 T 19870226; EP 87102736 A 19870226; FI 870901 A 19870302; SU 4202125 A 19870313; ZA 871394 A 19870226