

Title (en)
SURGE ARRESTER

Publication
EP 0242688 B1 19900718 (DE)

Application
EP 87105067 A 19870406

Priority
DE 3613599 A 19860422

Abstract (en)
[origin: US4739439A] An overvoltage arrester for handling high surge currents including a pair of electrodes one of which includes a blind bore into which the second electrode projects, thereby providing at least one discharge gap between the two electrodes. The arrester of the present invention has a long useful life and a high surge current carrying capability.

IPC 1-7
H01T 1/24; **H01T 4/12**

IPC 8 full level
H01T 1/22 (2006.01); **H01T 1/24** (2006.01); **H01T 4/12** (2006.01)

CPC (source: EP KR US)
H01C 7/12 (2013.01 - EP US); **H01T 1/24** (2013.01 - EP US); **H01T 4/00** (2013.01 - KR); **H01T 4/12** (2013.01 - EP US)

Citation (examination)

- US 3588576 A 19710628 - KAWIECKI CHESTER J
- US 3989985 A 19761102 - LANGE GERHARD, et al
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- AU 183813 A

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DE19632417C1; US9614370B2; WO2013152948A1

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EP 0242688 A1 19871028; **EP 0242688 B1 19900718**; DE 3763733 D1 19900823; JP S62256394 A 19871109; KR 870010658 A 19871130; KR 950009084 B1 19950814; US 4739439 A 19880419

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EP 87105067 A 19870406; DE 3763733 T 19870406; JP 9611687 A 19870417; KR 870003842 A 19870422; US 3866287 A 19870415