

Title (en)

CIRCUIT ARRANGEMENT FOR THE HIGH-FREQUENCY OPERATION OF LOW-PRESSURE DISCHARGE LAMPS

Publication

EP 0244644 B1 19910410 (DE)

Application

EP 87104991 A 19870403

Priority

DE 3611611 A 19860407

Abstract (en)

[origin: US4782268A] To suppress introduction of harmonics into the power supply network of a high-frequency operated fluorescent lamp, an input capacitor (C2) connected across a direct current supply (P1, P2) for a transistor push-pull high-frequency circuit-operating at between 25-50 kHz-has a pair of serially connected diodes (D4, D5) connected thereto. The junction point (M2) of the diodes is connected via a coupling capacity (C7) to a common junction (M1) of the push-pull connected transistors (T1, T2). The power line circuit has a line choke (L2, L2') connected, either in advance or behind a rectifier (G1) supplying direct current to the transistor push-pull circuit. The circuit effectively suppresses harmonics, particularly the third harmonic, without introducing additional losses in the circuit, especially if a further capacitor (C8) is connected in the resonance circuit of the fluorescent lamp and to the diode junction (M2) between the two diodes (D4, D5).

IPC 1-7

H05B 41/29

IPC 8 full level

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CPC (source: EP US)

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Cited by

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