Title (en)
INTERFACE, FOR EXAMPLE FOR A LIQUID CRYSTAL DISPLAY DEVICE

Publication
EP 0244978 A3 19890104 (EN)

Application
EP 87303469 A 19870421

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- JP 21299686 A 19860910
- JP 22065186 A 19860918

Abstract (en)
[origin: EP0244978A2] An interface for converting separate video signals into a display data signal and a timing signal for operating a thin display panel comprises a timing circuit (101, 102, 103) for taking a timing so as to introduce effective display data into a data treating circuit (105) according to a synchronizing signal (Hsyc), a data treating circuit (105) for generating desired data for a thin display panel using the display data, and a timing signal generating circuit (104) for generating timing signals necessary for operating a driver of the thin display panel.

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G09G 3/36

IPC 8 full level
G09G 3/20 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP)
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Citation (search report)
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