

Title (en)
INTERFACE, FOR EXAMPLE FOR A LIQUID CRYSTAL DISPLAY DEVICE

Publication
EP 0244978 A3 19890104 (EN)

Application
EP 87303469 A 19870421

Priority
• JP 9631386 A 19860425
• JP 12644086 A 19860530
• JP 21299686 A 19860910
• JP 22065186 A 19860918

Abstract (en)
[origin: EP0244978A2] An interface for converting separate video signals into a display data signal and a timing signal for operating a thin display panel comprises a timing circuit (101, 102, 103) for taking a timing so as to introduce effective display data into a data treating circuit (105) according to a synchronizing signal (Hsync), a data treating circuit (105) for generating desired data for a thin display panel using the display data, and a timing signal generating circuit (104) for generating timing signals necessary for operating a driver of the thin display panel.

IPC 1-7
G09G 3/36

IPC 8 full level
G09G 3/20 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP)
G09G 3/2096 (2013.01); **G09G 3/3607** (2013.01); **G09G 3/3611** (2013.01); **G09G 3/3692** (2013.01); **G09G 2310/0297** (2013.01); **G09G 2340/0471** (2013.01); **G09G 2340/0478** (2013.01)

Citation (search report)
• [A] EP 0171547 A2 19860219 - ASCII CORP [JP]
• [A] US 4275421 A 19810623 - LOUIE ANTHONY C H, et al
• [A] US 4536856 A 19850820 - HIROISHI AKIHIKO [JP]
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• [AP] EP 0195203 A2 19860924 - ASCII CORP [JP], et al
• [A] EP 0155488 A2 19850925 - IBM [US]
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Designated contracting state (EPC)
DE FR GB IT SE

DOCDB simple family (publication)
EP 0244978 A2 19871111; **EP 0244978 A3 19890104**; **EP 0244978 B1 19921104**; DE 3782450 D1 19921210; DE 3782450 T2 19930318

DOCDB simple family (application)
EP 87303469 A 19870421; DE 3782450 T 19870421