

Title (en)

SIGNAL SOURCE DISTORTION COMPENSATOR.

Title (de)

VERZERRUNGSKOMPENSATOR FÜR EINE SIGNALQUELLE.

Title (fr)

COMPENSATEUR DE LA DISTORSION D'UNE SOURCE DE SIGNAUX.

Publication

**EP 0250553 A4 19891212 (EN)**

Application

**EP 87900509 A 19861210**

Priority

US 81003485 A 19851217

Abstract (en)

[origin: WO8703967A1] A circuit for compensating for the varying amplitude, frequency and/or phase characteristics of the signal source pulses during operation. In the preferred embodiment the circuit includes a tapped delay line (20) to which received signal samples are supplied, the delayed output from each of the taps being weighted in accordance with a suitable weight generator circuit (24) and the weighted signals being appropriately summed to supply an output signal to the receiver.

IPC 1-7

**G01S 7/40**

IPC 8 full level

**G01S 7/288** (2006.01); **G01S 7/40** (2006.01)

CPC (source: EP)

**G01S 7/288** (2013.01)

Citation (search report)

- [Y] US 4089001 A 19780509 - DONAHUE THOMAS HARBISON
- [Y] US 4095224 A 19780613 - DOUNCE ERIC A, et al
- [E] US 4768035 A 19880830 - THURBER ROBERT E [US], et al
- See references of WO 8703967A1

Designated contracting state (EPC)

BE CH DE FR IT LI NL SE

DOCDB simple family (publication)

**WO 8703967 A1 19870702**; AU 6837787 A 19870715; EP 0250553 A1 19880107; EP 0250553 A4 19891212; GB 2192111 A 19871231; GB 2192111 B 19900328; GB 8718794 D0 19870916; IL 80889 A0 19870331; JP S63502136 A 19880818

DOCDB simple family (application)

**US 8602658 W 19861210**; AU 6837787 A 19861210; EP 87900509 A 19861210; GB 8718794 A 19861210; IL 8088986 A 19861205; JP 50039287 A 19861210