

Title (en)
DEVICE FOR PROGRAMMING A NON VOLATILE MEMORY OF A TIME PIECE

Publication
EP 0253227 B1 19891129 (FR)

Application
EP 87109566 A 19870703

Priority
CH 277386 A 19860710

Abstract (en)
[origin: US4763309A] This programming arrangement for a non-volatile memory incorporated in the inner circuit of a timepiece for adjusting the frequency of the time base thereof includes a support provided with a connector to be plugged in in place of the energy cell. The rate of division of the frequency divider is adjusted by the introduction into the memory of a number k representative of the frequency difference between the time base frequency and a standard frequency. To effect this the arrangement comprises an electronic circuit external to the timepiece and which is coupled thereto by the connector. The electronic circuit introduces the number k into certain predetermined stages of the divider. When such number k is thus introduced the state of such stages is blocked by the inner circuit of the timepiece such state then being transferred into the non-volatile memory.

IPC 1-7
G04D 7/00; **G04G 3/02**

IPC 8 full level
G04C 3/14 (2006.01); **G04D 7/00** (2006.01); **G04G 3/02** (2006.01)

CPC (source: EP US)
G04D 7/003 (2013.01 - EP US); **G04G 3/022** (2013.01 - EP US)

Citation (examination)
US 4147022 A 19790403 - ICHIKAWA SINGO

Cited by
EP1962156A1; EP0635771A4; EP0511573A3; US5289452A; USRE38197E; EP1890204A1; KR101390577B1; EP0342821A3; EP2916193A1; US8909847B2; US8149651B2; WO2008019819A1; WO0188635A3; WO2008101890A1; US6545950B1; US9671759B2; US6522601B2; US6754138B2; EP0347249B1; TWI448846B

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