

Title (en)

GRAPHICS BOARD WITH KEYBOARD AND MOUSE INTERFACE

Publication

EP 0255436 B1 19910522 (FR)

Application

EP 87401744 A 19870724

Priority

FR 8610875 A 19860728

Abstract (en)

[origin: EP0255436A1] The present invention relates to an interface and graphics board between a central processor 1 communicating asynchronously with this board via an external bus and the video monitor 9 characterised in that it comprises: - a set of video-memories 45, 46 with capacity equal to at least the number of desired display points on the monitor 9, - at least one group of random-access memories 43, 44 or 41, 42 for storing graphics application software, - a circuit 40 controlling the video system, and managing the addresses of the various memories with a view to refreshing the memories and the video display, and updating the data or the programs and delivering the video signals; - a circuit 60 for serialising the data delivered by the video memory 45, 46 in step with a video clock H1; - a graphics processor 30 kept in step by a clock H0 and operating in synchronism with the circuit 40 of the controller of the video system so as to execute the software programs contained in the group of random-access memories; - an interface circuit 20 to 22 between the external bus and the buses of the board and a circuit 23 to 27 for arbitration between the graphics processor 30, the video-system controller 40 and the external bus. <IMAGE>

IPC 1-7

G09G 1/16

IPC 8 full level

G09G 5/36 (2006.01); **G09G 5/39** (2006.01)

CPC (source: EP)

G09G 5/363 (2013.01)

Cited by

US6119207A; US6145033A; EP0483467A3; US6204864B1; WO9641325A1

Designated contracting state (EPC)

BE DE ES FR GB IT NL SE

DOCDB simple family (publication)

EP 0255436 A1 19880203; **EP 0255436 B1 19910522**; DE 3770212 D1 19910627; ES 2022402 B3 19911201; FR 2602892 A1 19880219; FR 2602892 B1 19881110

DOCDB simple family (application)

EP 87401744 A 19870724; DE 3770212 T 19870724; ES 87401744 T 19870724; FR 8610875 A 19860728