

Title (en)
DISPLAY DEVICE

Publication
EP 0260747 B1 19910703 (EN)

Application
EP 87201715 A 19870910

Priority
NL 8602328 A 19860915

Abstract (en)
[origin: EP0260747A1] The invention relates to a novel addressing scheme for passive matrices in which voltage losses across the electrodes are compensated. The row electrodes (3) of a matrix of display elements (12) are alternately driven from the left and from the right while simultaneously presenting data voltages to the column electrodes (4).

IPC 1-7
G09G 3/36

IPC 8 full level
G02F 1/133 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP KR)
G09G 3/36 (2013.01 - KR); **G09G 3/3622** (2013.01 - EP)

Cited by
EP0344323A4; US6091392A; US8878829B2

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0260747 A1 19880323; **EP 0260747 B1 19910703**; CN 1012857 B 19910612; CN 87106311 A 19880330; DE 3771154 D1 19910808; JP S6375732 A 19880406; KR 880004418 A 19880603; NL 8602328 A 19880405

DOCDB simple family (application)
EP 87201715 A 19870910; CN 87106311 A 19870912; DE 3771154 T 19870910; JP 23088487 A 19870914; KR 870010150 A 19870914; NL 8602328 A 19860915