Title (en)
UNIVERSAL PROGRAMMABLE COUNTER/TIMER AND ADDRESS REGISTER MODULE.

Title (de)
UNIVERSELL PROGRAMMIERBARES ZÄHLER-ZEITGEBER- UND ADRESSREGISTER-MODUL.

Title (fr)
MODULE PROGRAMMABLE UNIVERSEL SERVANT DE COMPTEUR/TEMPORISATEUR ET DE REGISTRE D'ADRESSES.

Publication
EP 0269676 A4 19891228 (EN)

Application
EP 87903568 A 19870504

Priority
US 86015886 A 19860506

Abstract (en)
[origin: WO8707054A1] Universal programmable modules provide timing and/or address register functions for a broad family of test instruments; such as counter timers, arbitrary function generators, and real time digitizers. The computer programmable apparatus (20) can be used as an accumulator, a time base generator or a memory address register. A clock input (50) receives a clock input signal. A computer input (32), which may include a bidirectional data bus (30), receives inputs from a computer. A processor (24) processes at least one of the clock input signal and the input from the computer to produce at least one output (56, 58, 78). A control circuit (50) controls the processor in accordance with further input from the computer to determine the output produced. An output selector (28), which may include a multiplexer (40) for directly addressing memory, selects at least one output to be used externally of the apparatus.

IPC 1-7
G06F 9/00

IPC 8 full level
G01R 31/28 (2006.01); G01R 31/319 (2006.01); G06F 1/03 (2006.01); G06F 1/14 (2006.01); G06F 11/22 (2006.01); G06F 13/38 (2006.01)

CPC (source: EP)
G01R 31/31919 (2013.01); G01R 31/31922 (2013.01); G06F 1/0328 (2013.01); G06F 1/14 (2013.01); G06F 13/38 (2013.01)

Citation (search report)
- [A] US 4127823 A 19781128 - FROST R JACK
- [XP] ELECTRONICS & WIRELESS WORLD, vol. 92, no. 1607, August 1986, pages 18-23, London, GB; M. CATHERWOOD: "Microcontroller chip includes peripherals"
- [XP] ELEKTRONIK, vol. 36, no. 4, 20th February 1987, pages 69-74, Munich, DE; H. ANDERS: "Der Anwender bestimmt wesentliche Chip-Eigenschaften: Mikrocontroller - nicht, "von der Stange""
- See references of WO 8707054A1

Designated contracting state (EPC)
DE FR GB IT NL SE

DOCDB simple family (publication)
WO 8707054 A1 19871119; CA 1286785 C 19910723; EP 0269676 A1 19880608; EP 0269676 A4 19891228; JP 2592878 B2 19970319; JP S63503251 A 19881124

DOCDB simple family (application)
US 8701038 W 19870504; CA 536269 A 19870504; EP 87903568 A 19870504; JP 50309687 A 19870504