

Title (en)
POWER DIVIDER/COMBINER CIRCUIT

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Application
EP 87902973 A 19870330

Priority
US 86821186 A 19860528

Abstract (en)
[origin: WO8707438A1] An N-way, broad band planar power divider/combiner circuit (10) for dividing or combining RF signals which includes a tapered strip (1) of electrically conductive material having a plurality of conductor fingers (5) which define a plurality of ports (6) at the wide end (3) of the taper, and having a narrow end (2) which defines single port. The tapered metal strip is mounted onto a dielectric slab (8), and isolation resistors (7) connect adjacent fingers. A single RF signal can be fed into the single port which will be divided into a plurality of signals of equi-amplitude and equi-phase. Conversely, a plurality of RF signals can be fed into the ports at the wide end which will be combined into a single signal.

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H01P 5/12 (2006.01); **H01P 5/19** (2006.01)

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