

Title (en)
DISPLAY CONTROLLER FOR DATA PROCESSING APPARATUSES

Publication
EP 0272006 A3 19891018 (EN)

Application
EP 87310229 A 19871119

Priority
IT 6793286 A 19861216

Abstract (en)
[origin: EP0272006A2] The controller comprises a refresh memory (23) which can be activated under the control of a window mode signal for selectively storing a character code or a map of graphic points in each video display cell, corresponding to the alpha-numeric character matrix. An auxiliary memory (33) then stores for each cell a code which is indicative of the alpha-numeric or graphic mode in accordance with which the cell has been recorded, whereby a series of windows which are recorded in different ways can be simultaneously displayed under control of a control unit (19) which sets up the windows. The controller can operate in window mode with a monochrome or colour display. A positive character generator (34) can be selected to display of the characters in black on a white background.

IPC 1-7
G09G 1/16

IPC 8 full level
G06F 3/14 (2006.01); **G06F 3/048** (2013.01); **G09G 5/14** (2006.01); **G09G 5/40** (2006.01)

CPC (source: EP)
G09G 5/14 (2013.01); **G09G 5/40** (2013.01)

Citation (search report)
• [Y] WO 8605910 A1 19861009 - BRITISH TELECOMM [GB]
• [Y] EP 0071744 A2 19830216 - IBM [US]
• [A] EP 0175342 A2 19860326 - HONEYWELL INF SYSTEMS [US]
• [A] EP 0153789 A2 19850904 - PHILIPS ELECTRONIC ASSOCIATED [GB], et al

Designated contracting state (EPC)
CH DE ES FR GB LI NL SE

DOCDB simple family (publication)
EP 0272006 A2 19880622; EP 0272006 A3 19891018; IT 1196844 B 19881125; IT 8667932 A0 19861216; JP S63233425 A 19880929

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