Title (en)

Current mirror circuit.

Title (de)

Stromspiegelschaltung.

Title (fr)

Circuit de miroir de courant.

Publication

EP 0275582 A1 19880727 (DE)

Application

EP 87202445 A 19871208

Priority

DE 3642167 A 19861210

Abstract (en)

[origin: US4812734A] A current-mirror arrangement comprising a first branch including two series-connected diodes and a second branch including the series-connected base-emitter paths of two transistors. The ratio between the input and output currents of the current mirror is proportional to the root of the current-gain factor of the transistors.

Abstract (de)

Die Erfindung betrifft eine Stromspiegelschaltung mit einem ersten Zweig, der zwei in Serie geschaltete Dioden enthält, und einem zweiten Zweig, der die in Serie geschalteten Basis-Emitter-Strecken zweier Transistoren enthält. Das Verhältnis der Ein- und Ausgangsströme ist dabei der Wurzel des Stromverstärkungsfaktors der Transistoren proportional.

IPC 1-7

G05F 3/26

IPC 8 full level

G05F 3/26 (2006.01)

CPC (source: EP US)

G05F 3/265 (2013.01 - EP US)

Citation (search report)

- [XD] DE 3035272 A1 19810402 - RCA CORP [US]
- [X] US 4507573 A 19850326 - NAGANO KATSUMI [JP]
- [A] US 3868581 A 19750225 - AHMED ADEL ABDEL AZIZ
- [A] US 4103249 A 19780725 - BURDICK KENNETH JOHN
- [A] RCA TECHNICAL NOTES, Nr. 990, 1974, Seiten 1-7, Princeton, New Jersey, US; O.H. SCHADE, Jr.: "Current-mirror amplifiers having current gains less influenced by the base currents of components transistors"

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

DE 3642167 A1 19880630; DE 3774686 D1 19920102; EP 0275582 A1 19880727; EP 0275582 B1 19911121; JP 2628663 B2 19970709; JP S63157215 A 19880630; US 4812734 A 19890314

DOCDB simple family (application)

DE 3642167 A 19861210; DE 3774686 T 19871208; EP 87202445 A 19871208; JP 30975287 A 19871209; US 12526287 A 19871125