

Title (en)  
CURRENT MIRROR CIRCUIT

Publication  
**EP 0275582 B1 19911121 (DE)**

Application  
**EP 87202445 A 19871208**

Priority  
DE 3642167 A 19861210

Abstract (en)  
[origin: US4812734A] A current-mirror arrangement comprising a first branch including two series-connected diodes and a second branch including the series-connected base-emitter paths of two transistors. The ratio between the input and output currents of the current mirror is proportional to the root of the current-gain factor of the transistors.

IPC 1-7  
**G05F 3/26**

IPC 8 full level  
**G05F 3/26** (2006.01)

CPC (source: EP US)  
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Designated contracting state (EPC)  
DE FR GB IT

DOCDB simple family (publication)  
**DE 3642167 A1 19880630**; DE 3774686 D1 19920102; EP 0275582 A1 19880727; EP 0275582 B1 19911121; JP 2628663 B2 19970709; JP S63157215 A 19880630; US 4812734 A 19890314

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