

Title (en)  
DEVICE FOR REGISTERING CAR CALLS FOR LIFTS

Publication  
**EP 0279928 B1 19901031 (DE)**

Application  
**EP 87118051 A 19871207**

Priority  
CH 67187 A 19870223

Abstract (en)  
[origin: EP0279928A1] 1. Device for registering car calls for a lift with floor call memories (16) and floor call buttons (DE), with cage call memories (10) and cage call buttons (DC), with a scanning device (13, 14, 17), which scans the cage call memories (10) and the floor call memories (16) for the purpose of ascertaining memorised calls, wherein a first and a second signal sequence (BC-Z, BE-Z) are generated, which contain the memorised calls, and with a control device (9), into which a cage call or a floor call is transferable only during the standstill of the lift cage (2) at a floor and which generates travel direction signals and stop signals for direct travel to chosen floors, characterised thereby, - that the scanning device (13, 14, 17) displays a counter (14), which generates addresses associated with the floors, - that address conductors (A0, A1, A2, A3) connected to the parallel outputs of the counter (14) and a conductor carrying the second signal sequence (BE-Z) are connected with the input of a first gate arrangement (18), - that a shift register (30) is provided for each address bit and the outputs of the first gate arrangement (18) are connected with the inputs of the shift registers (30), wherein the associated address is fed to the inputs of the shift register (30) on the appearance of a floor call in the second signal sequence (BE-Z), - that a second gate arrangement (20) is provided, the inputs of which are connected with the outputs of the shift registers (30) and with a releasing circuit (22), - that the outputs of the second gate arrangement (20) are connected to the control device (9), wherein an address present at the outputs of the shift registers (30) cannot be transferred into the control device (9) until the arrival of a clearance signal (~G~C), which signals the absence of a cage call, of the releasing circuit (22), - that the shift registers (30) are connected with an erasure switching circuit (31), wherein the last memories (30.15) of the shift registers (30) are reset and further addresses memorised in the shift registers (30) are shifted onwards after transfer of the address present at the outputs of the shift registers (30) into the control device (9) and - that the shift registers (30) are connected with an input switching circuit (32), wherein the address present at the inputs of the shift registers (30) is entered into the first memories (30.1) of the shift registers (30) if these first memories (30.1) are empty and the address concerned is not yet memorised in the shift registers (30).

IPC 1-7  
**B66B 1/16; B66B 1/46**

IPC 8 full level  
**B66B 1/00** (2006.01); **B66B 1/16** (2006.01); **B66B 1/46** (2006.01); **B66B 5/00** (2006.01)

CPC (source: EP)  
**B66B 1/16** (2013.01); **B66B 1/468** (2013.01)

Cited by  
EP4104805A1; US11723814B2

Designated contracting state (EPC)  
AT CH DE ES FR GB IT LI

DOCDB simple family (publication)  
**EP 0279928 A1 19880831; EP 0279928 B1 19901031**; AT E57890 T1 19901115; CN 1013096 B 19910710; CN 88100489 A 19881005; DE 3765932 D1 19901206; ES 2018813 B3 19910516; HU 197703 B 19890529; HU T45943 A 19880928; IN 168430 B 19910330

DOCDB simple family (application)  
**EP 87118051 A 19871207**; AT 87118051 T 19871207; CN 88100489 A 19880201; DE 3765932 T 19871207; ES 87118051 T 19871207; HU 556787 A 19871210; IN 939MA1987 A 19871229