

Title (en)
AUDIO OUTPUT AMPLIFIER

Publication
EP 0280327 B1 19921125 (EN)

Application
EP 88102916 A 19880226

Priority
JP 4454287 A 19870227

Abstract (en)
[origin: EP0280327A2] A stand-by signal is supplied to a stand-by circuit (23) for determining the operation condition is a normal operative mode or a stand-by mode to control the operation. An output signal of the stand-by circuit (23) is delayed by a delay circuit. An output signal from the delay circuit is supplied to a bias circuit for generating a predetermined bias voltage. An audio signal supplied from the audio signal input terminal (14) is amplified by an amplifier circuit (26) based on the bias voltage from the bias circuit. When it is detected by an overcurrent detection circuit (27) that an overcurrent flows in the amplifier circuit (26), supply of the bias voltage from the bias circuit to the amplifier circuit (26) is interrupted by the protection circuit (25). The operation of the protection circuit (25) is controlled by output signals from the delay circuit and the overcurrent detection circuit (27).

IPC 1-7
H03F 1/52

IPC 8 full level
H03F 3/30 (2006.01); **H03F 1/42** (2006.01); **H03F 1/52** (2006.01)

CPC (source: EP KR US)
H03F 1/52 (2013.01 - EP US); **H03F 3/30** (2013.01 - KR)

Cited by
EP1154568A3; FR2741212A1; EP0515853A1; US5224169A; TR26900A; GB2251350A; US5255094A; GB2251350B; CN1066599C

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0280327 A2 19880831; **EP 0280327 A3 19890426**; **EP 0280327 B1 19921125**; DE 3876113 D1 19930107; DE 3876113 T2 19930415; HK 129293 A 19931203; JP H0618294 B2 19940309; JP S63211905 A 19880905; KR 880010561 A 19881010; KR 900007923 B1 19901023; US 4821000 A 19890411

DOCDB simple family (application)
EP 88102916 A 19880226; DE 3876113 T 19880226; HK 129293 A 19931125; JP 4454287 A 19870227; KR 880002116 A 19880227; US 1608388 A 19880226