

Title (en)
FREQUENCY-MODULATED TRACK CURRENT CIRCUIT

Publication
EP 0282932 A3 19891206 (DE)

Application
EP 88103941 A 19880312

Priority
DE 3708788 A 19870318

Abstract (en)
[origin: EP0282932A2] A circuit is described in which the bandwidth of the track circuit receiver (8) is design to be so narrow that it is virtually impossible for it to be subjected to interference. To permit interpretation of the frequency-modulated track voltage, the frequency deviation of which far exceeds the bandwidth of the receiver, the narrow band characteristic is achieved by means of an adjustable bandpass filter (7), the centre frequency of which is controlled so that it precisely follows the frequency of the track circuit transmitter (2). For this purpose, the output signal from the track circuit transmitter is fed to the track circuit receiver via a special lead (9). <IMAGE>

IPC 1-7
B61L 1/18

IPC 8 full level
B61L 1/18 (2006.01)

CPC (source: EP)
B61L 1/188 (2013.01)

Citation (search report)

- [Y] FR 2121265 A5 19720818 - INT STANDARD ELECTRIC CORP
- [Y] DE 3325249 A1 19850131 - STANDARD ELEKTRIK LORENZ AG [DE]
- [Y] DE 2140381 A1 19720217 - GEN SIGNAL CORP
- [A] DE 3403946 A1 19850808 - STANDARD ELEKTRIK LORENZ AG [DE]
- [A] US 4417229 A 19831122 - WILSON H JAMES [US]

Cited by
CN102341290A; US8613410B2; WO2010100055A1

Designated contracting state (EPC)
AT CH DE ES LI LU

DOCDB simple family (publication)
EP 0282932 A2 19880921; EP 0282932 A3 19891206; EP 0282932 B1 19960710; AT E140194 T1 19960715; DE 3708788 A1 19880929; DE 3855407 D1 19960814; ES 2091745 T3 19961116; TR 24261 A 19910729; YU 53188 A 19901231

DOCDB simple family (application)
EP 88103941 A 19880312; AT 88103941 T 19880312; DE 3708788 A 19870318; DE 3855407 T 19880312; ES 88103941 T 19880312; TR 19988 A 19880316; YU 53188 A 19880316