

Title (en)

## APPARATUS FOR BUMP-PLATING CHIPS

Publication

**EP 0283681 B1 19920506 (DE)**

Application

**EP 88101499 A 19880202**

Priority

DE 3705727 A 19870223

Abstract (en)

[origin: EP0283681A1] The object is to design a plating apparatus for producing finely structured, thick metal depositions on semiconductor wafers. At the same time, it is required that, in spite of an unfavourable starting base, the bump surface should be virtually flat and, in addition, a uniformity of +/- 1.0 μm should be achieved for the bump height. Furthermore, the plating apparatus must ensure reproducible, uniformly good metal deposition over many months. <??>To achieve this object, a plating cell having anode, cathode and shielding ring is used for the purpose of suspension in a plating bath. This cell consists of a plastic tube whose lower opening is provided with the anode surface and whose upper opening is covered by a wafer holder with the semiconductor wafer. The plating apparatus furthermore includes an active carbon filtering system adjusted for the levelling effect. <??>The field of application is micropack technology. <IMAGE>

IPC 1-7

**C25D 7/12**

IPC 8 full level

**C25D 17/00** (2006.01); **C25D 7/12** (2006.01); **C25D 17/08** (2006.01); **C25D 17/12** (2006.01); **C25D 21/06** (2006.01); **C25D 21/18** (2006.01); **H01L 21/288** (2006.01); **H01L 21/60** (2006.01)

CPC (source: EP US)

**C25D 7/12** (2013.01 - EP US); **C25D 17/001** (2013.01 - EP US)

Cited by

EP1736569A1; EP0606610A3; US7780824B2

Designated contracting state (EPC)

CH DE FR LI

DOCDB simple family (publication)

**EP 0283681 A1 19880928**; **EP 0283681 B1 19920506**; DE 3870685 D1 19920611; JP H044399 B2 19920128; JP S63216998 A 19880909; US 4906346 A 19900306

DOCDB simple family (application)

**EP 88101499 A 19880202**; DE 3870685 T 19880202; JP 3735288 A 19880219; US 15331888 A 19880208