

Title (en)

MINIATURIZED SECONDARY ELECTRON MULTIPLIER AND ITS MANUFACTURING PROCEDURE

Publication

EP 0283773 A3 19900207 (DE)

Application

EP 88103116 A 19880302

Priority

DE 3709298 A 19870320

Abstract (en)

[origin: EP0283773A2] The invention relates to a secondary electron multiplier with discrete dynodes. The invention is based on the object, in comparison with the indicated prior art, of producing a miniaturised secondary electron multiplier, and arrays thereof, which require very little space, have good time resolution, good sensitivity and good flexibility for moulding. The object is achieved in that the dynodes are micro-structured and are fitted on an insulating substrate plate which is provided with electrical conductor tracks for the connection of the dynodes.

IPC 1-7

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IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

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- [A] US 4034255 A 19770705 - CATANESE CARMEN ANTHONY, et al
- [A] IEEE TRANSACTIONS ON NUCLEAR SCIENCE, Band NS-32, Nr. 6, Dezember 1985, Seiten 3996-4000, IEEE, New York, US; D.K. LYNN et al.: "Thermionic integrated circuits: electronics for hostile environments"

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EP0515261A1; FR2676862A1; US5367218A

Designated contracting state (EPC)

AT CH FR GB IT LI NL SE

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EP 0283773 A2 19880928; **EP 0283773 A3 19900207**; **EP 0283773 B1 19920520**; AT E76537 T1 19920615; DE 3709298 A1 19880929; DE 3709298 C2 19900208; JP S63279553 A 19881116; US 4990827 A 19910205

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